

DO NOT ENTER
10/30/2006 CW
P/3236-39

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Confirmation No.: 7918

Kenichi KOYANAGI, et. al.

Date: October 20, 2006

Serial No.: 10/630,444

Group Art Unit: 2823

Filed: July 29, 2003

Examiner: William D. COLEMAN

For: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

VIA EFS-WEB

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is a response to the Final Office Action mailed August 3, 2006 in the above-identified application. Reconsideration of the application is respectfully requested.

FEE CALCULATION

Any additional fee required has been calculated as follows:

If checked, "Small Entity" status is claimed.

NO. CLAIMS AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	EXTRA PRESENT	RATE	ADDIT. FEE
<u>TOTAL</u> 37	MINUS 37	* = 0	X (\$25 SE or \$50)	\$0
<u>INDEP.</u> 3	MINUS 3	** = 0	X (\$100 SE or \$200)	\$0
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM			X (\$180 SE or \$360)	\$0

* not less than 20

** not less than 3

TOTAL \$ 0

If any additional payment is required, credit card payment in the amount of \$ _____ is submitted via EFS-WEB.